

REMARKS

At the outset, the Examiner is thanked for the thorough review and consideration of the pending application. The Office Action dated July 25, 2006 has been received and its contents carefully reviewed.

By this Amendment, Applicant has amended claims 1, 6 and 13. Accordingly, claims 1-16 are current pending in the present application. Reexamination and reconsideration of the pending claims is respectfully requested.

In the Office Action, the Examiner rejected claims 1-16 under 35 U.S.C. § 102(e) as being anticipated by Miyazawa (U.S. Patent No. 6,858,991). This rejection is respectfully traversed and reconsideration is requested.

Claim 1 is allowable over the cited references in that each of these claim 1 recites a combination of elements including, for example, "...a power control driver supplying a power voltage to a source terminal of the first driving thin film transistor through the power line, the power voltage having a first value during an emitting time section of a single frame and a second value during a rest time section of the single frame." None of the cited references, singly or in combination, teaches or suggests at least this feature of the claimed invention. Accordingly, Applicant respectfully submits that claim 1 and claims 2-5, which depend from claim 1, are allowable over the cited references.

Claim 6 is allowable over the cited references in that each of these claim 6 recites a combination of elements including, for example, "...wherein gate, source and drain terminals of the driving thin film transistor are connected to the switching thin film transistor, a power line and the organic electroluminescent diode, respectively... a power control driver supplying a power voltage to the power line, the power voltage having a first value during an emitting time section of a single frame and a second value during a rest time section of the single frame." None of the cited references, singly or in combination, teaches or suggests at least this feature of the claimed invention. Accordingly, Applicant respectfully submits that claim 6 and claims 7-12, which depend from claim 6, are allowable over the cited references.

Claim 13 is allowable over the cited references in that each of these claim 13 recites a combination of elements including, for example, "...applying a first value of a power voltage through a source terminal of the driving transistor to an organic electroluminescent diode that is

connected to a drain terminal of the driving transistor during an emitting time section of a single frame; applying a second value of the power voltage through the source terminal of the driving transistor to the organic electroluminescent diode during a rest time section of the single frame.” None of the cited references, singly or in combination, teaches or suggests at least this feature of the claimed invention. Accordingly, Applicant respectfully submits that claim 13 and claims 14-16, which depend from claim 13, are allowable over the cited references.

Applicant believes the application is in condition for allowance and early, favorable action is respectfully solicited. If for any reason the Examiner finds the application other than in condition for allowance, the Examiner is requested to call the undersigned attorney at (202) 496-7500 to discuss the steps necessary for placing the application in condition for allowance. All correspondence should continue to be sent to the below-listed address.

If these papers are not considered timely filed by the Patent and Trademark Office, then a petition is hereby made under 37 C.F.R. §1.136, and any additional fees required under 37 C.F.R. §1.136 for any necessary extension of time, or any other fees required to complete the filing of this response, may be charged to Deposit Account No. 50-0911. Please credit any overpayment to deposit Account No. 50-0911. A duplicate copy of this sheet is enclosed.

Dated: October 25, 2006

Respectfully submitted,

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connected to a drain terminal of the driving transistor during an emitting time section of a single frame; applying a second value of the power voltage through the source terminal of the driving transistor to the organic electroluminescent diode during a rest time section of the single frame.” None of the cited references, singly or in combination, teaches or suggests at least this feature of the claimed invention. Accordingly, Applicant respectfully submits that claim 13 and claims 14-16, which depend from claim 13, are allowable over the cited references.

Applicant believes the application is in condition for allowance and early, favorable action is respectfully solicited. If for any reason the Examiner finds the application other than in condition for allowance, the Examiner is requested to call the undersigned attorney at (202) 496-7500 to discuss the steps necessary for placing the application in condition for allowance. All correspondence should continue to be sent to the below-listed address.

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